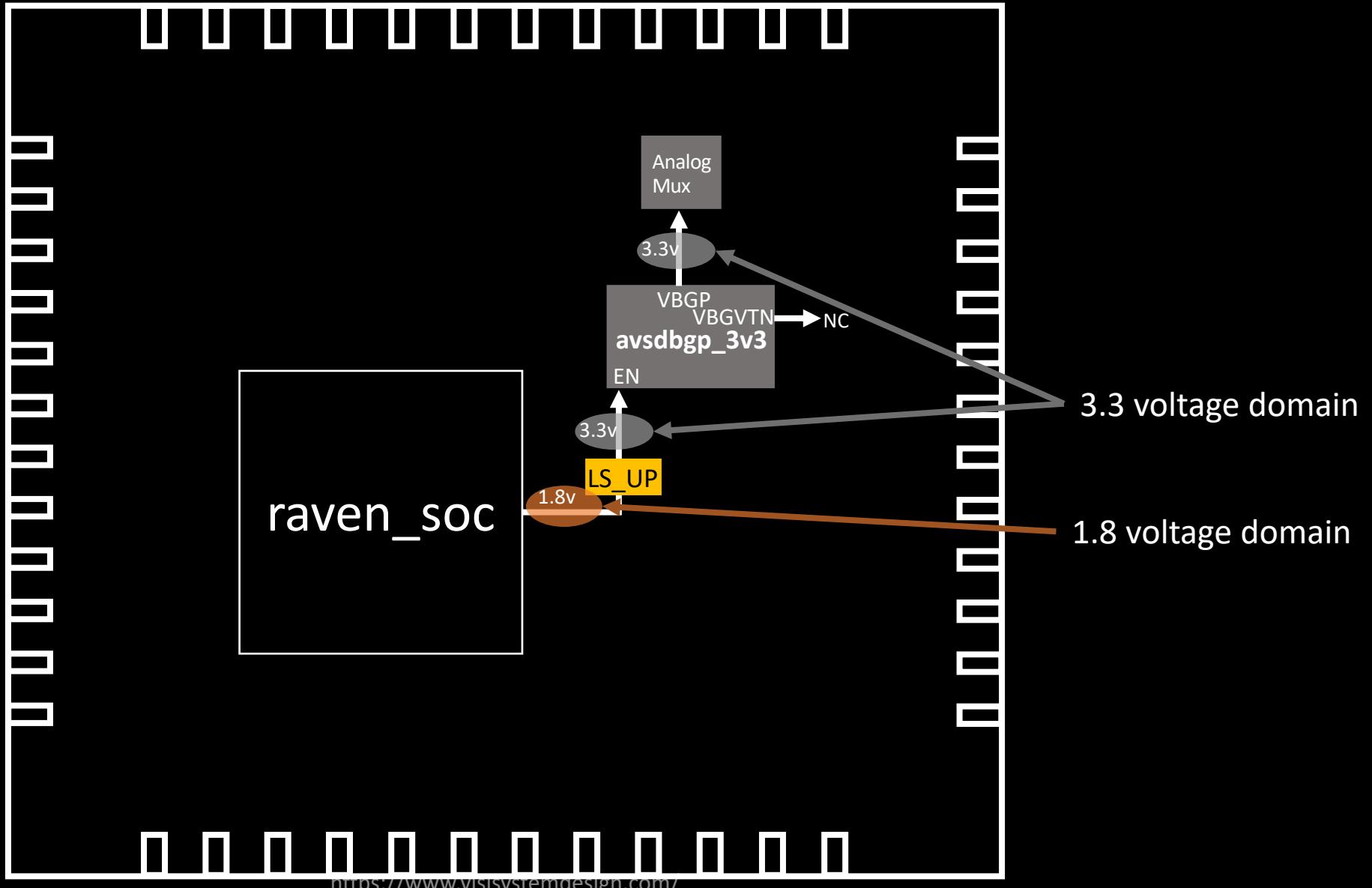


Bandgap (avsdbsgp_3v3) spec sheet for 180nm tech node

- Specs released under APACHE LICENSE 2.0
- Please contact Kunal at kunalpghosh@gmail.com in case of any doubts

Application Note for band-gap (avsdsgp_3v3)



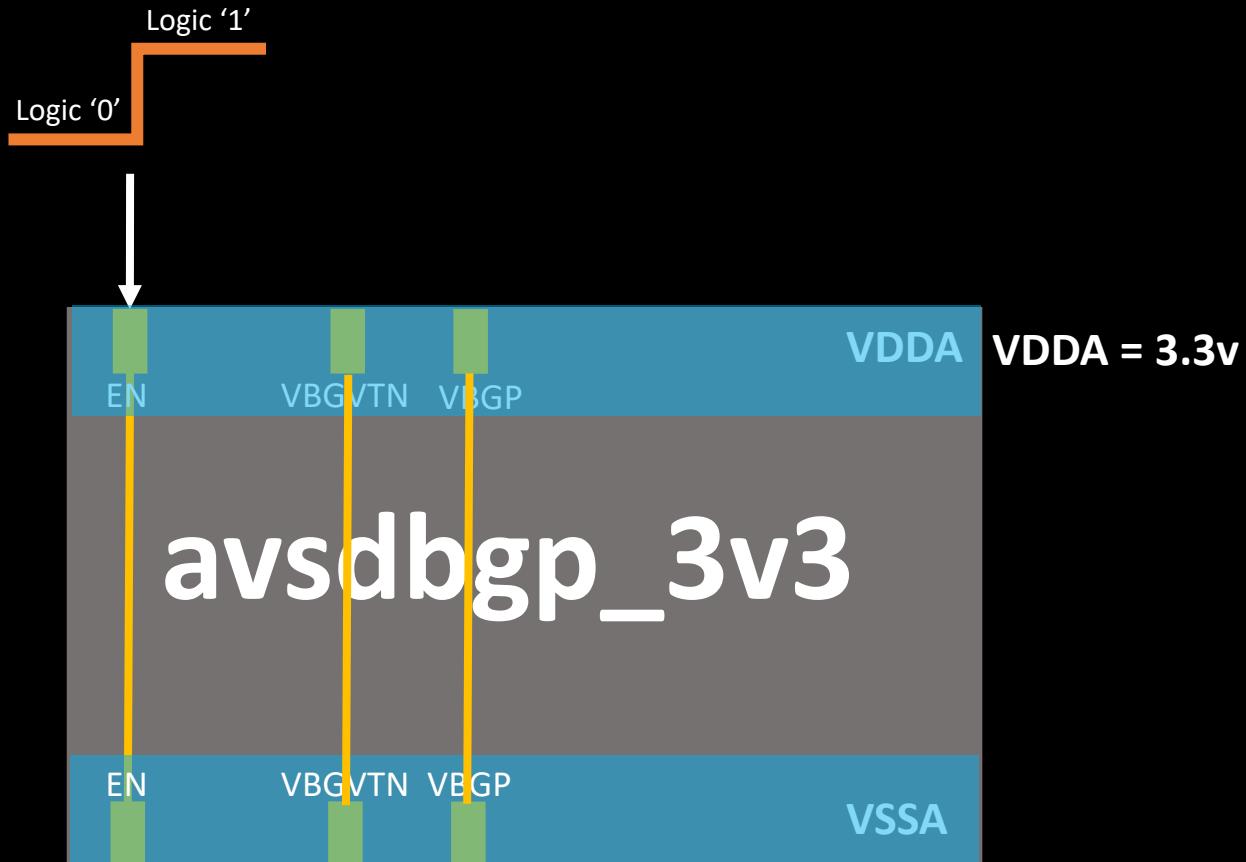
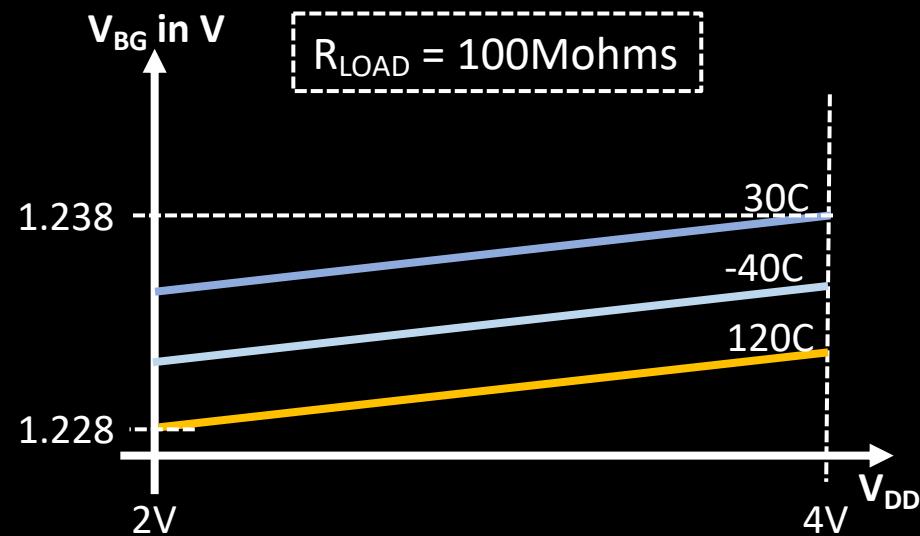
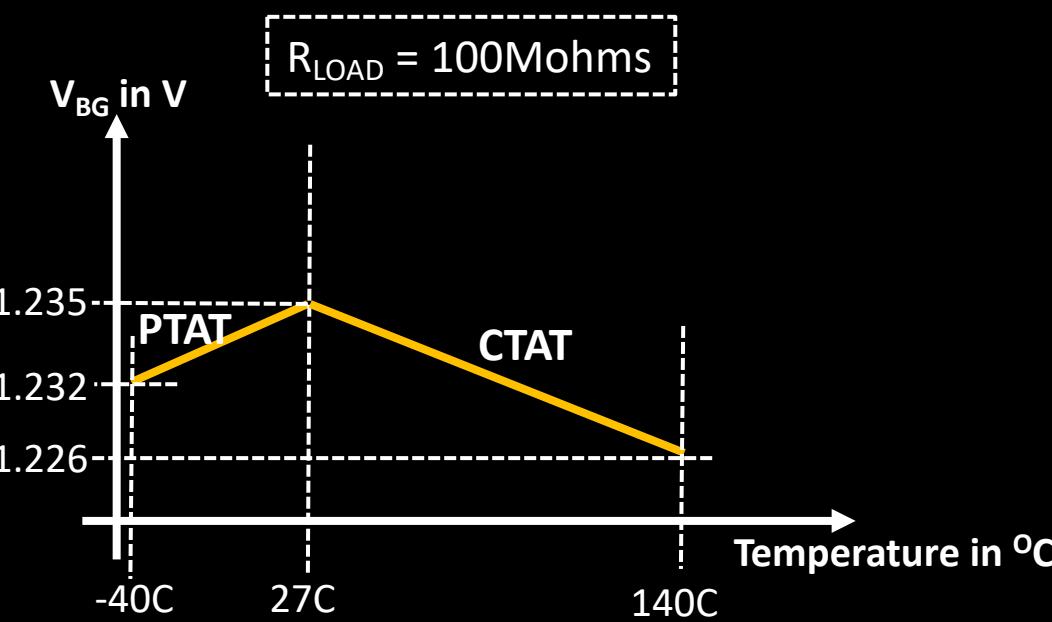
avsdsgp_3v3 preferred dimensions, pin locations and metal layers



■ Signal pins – metal2 (0.38um x 0.8um)

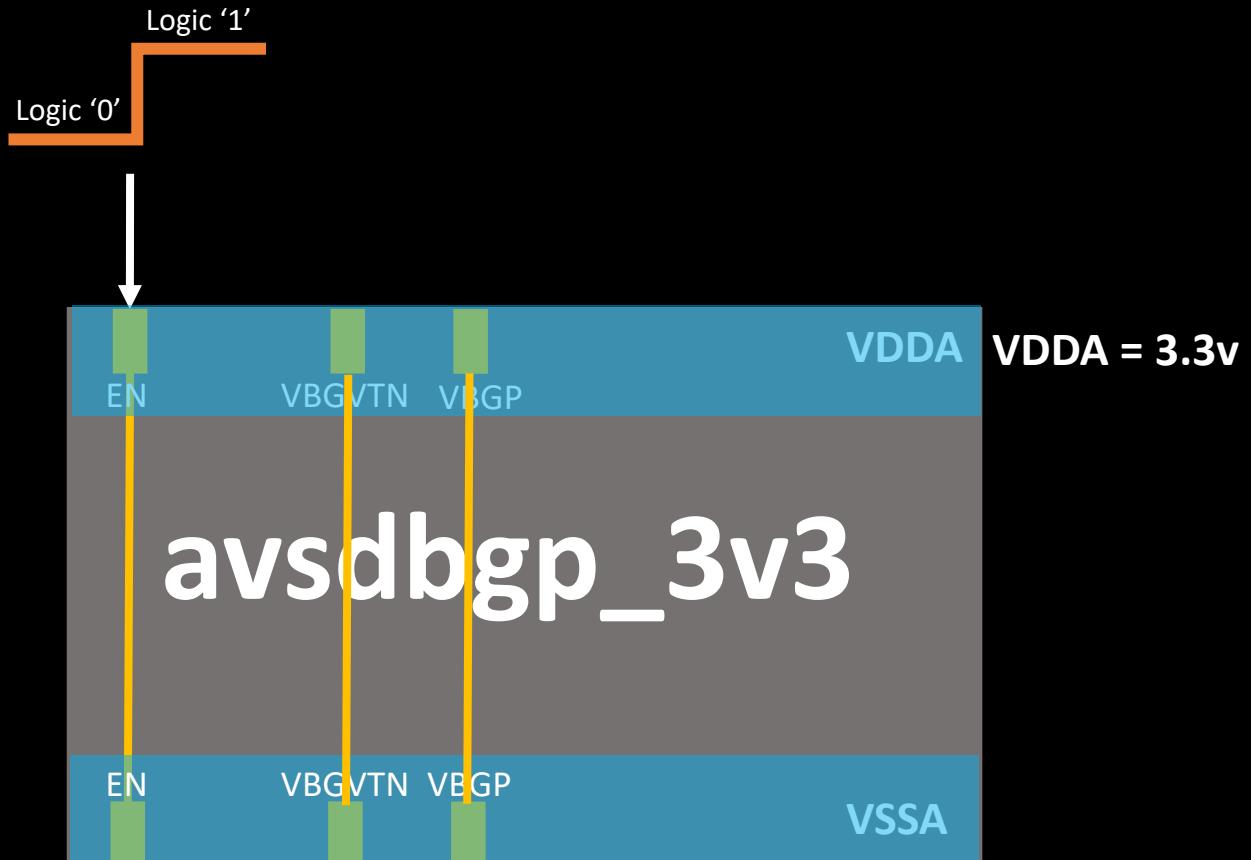
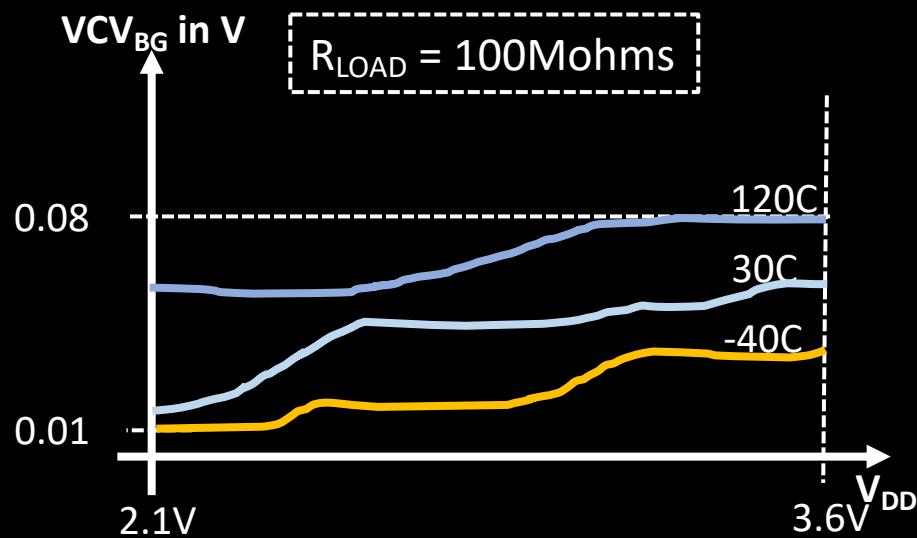
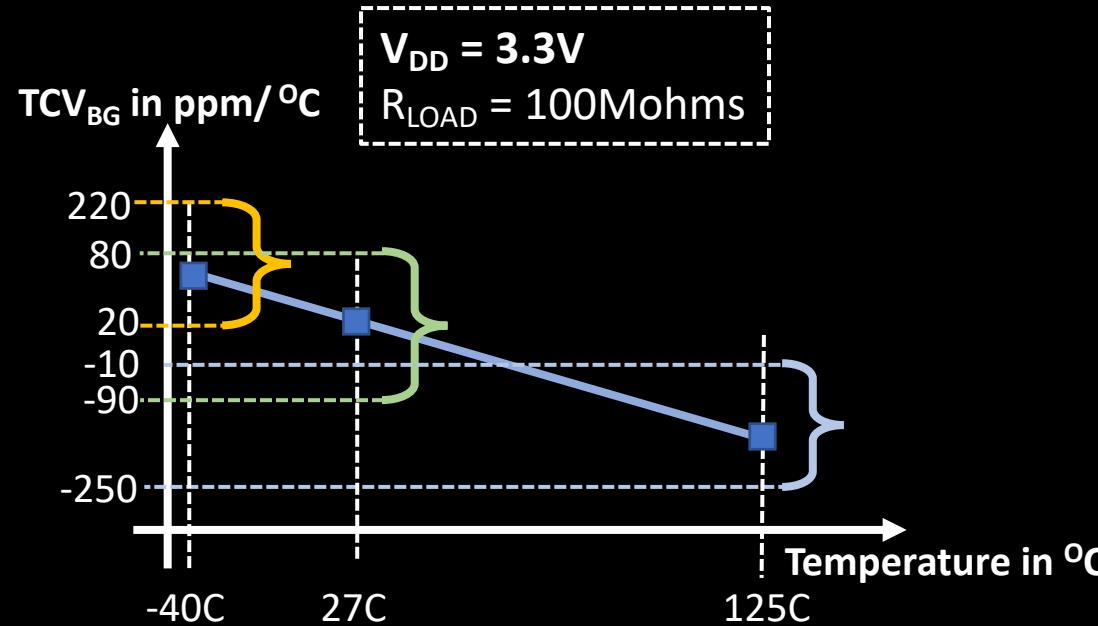
■ VDDA pins on metal3 (291.04um x 3um)
VSSA pins on metal1 (291.04um x 5um)

avsdbgp_3v3 operating modes



1.018v (For analog muxes)
(For current source and bias cells)
1.235v (For analog muxes)
(For current source and bias cells)

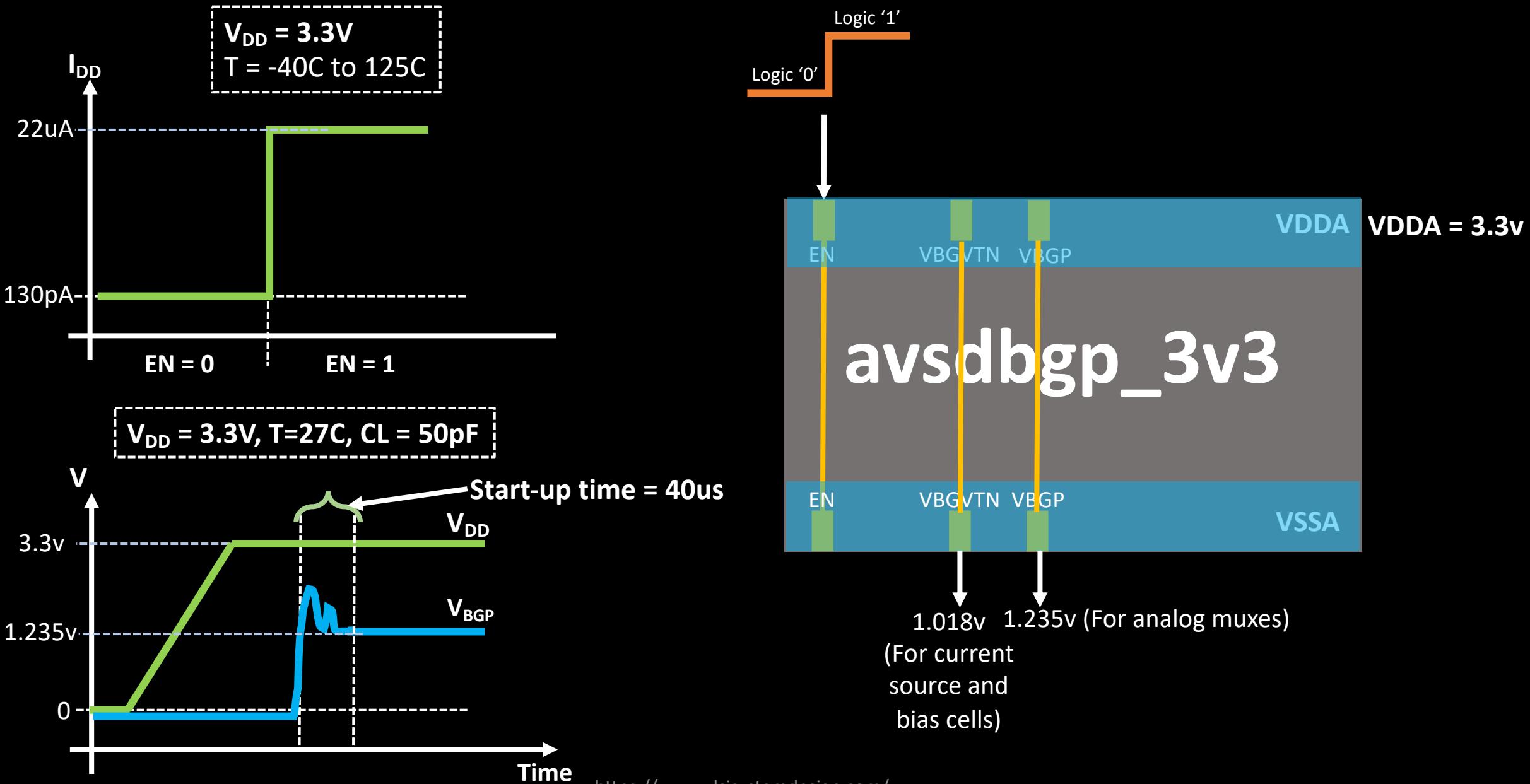
avsdbgp_3v3 operating modes



1.018v
(For current source and bias cells)

1.235v (For analog muxes)

avsdbgp_3v3 operating modes



avsdbgp_3v3 plots and values needed

- 1) V_{BGP} vs Temperature (-40C to 140C) at $R_{LOAD} = 100\text{Mohms}$
- 2) V_{BGP} vs V_{DD} (2V to 4V) at $R_{LOAD} = 100\text{Mohms}$
- 3) Temperature Co-efficient of V_{BGP} vs Temperature (-40C to 140C)
at $R_{LOAD} = 100\text{Mohms}$
- 4) Voltage Co-efficient of V_{BGP} vs V_{DD} (2V to 4V) at $R_{LOAD} = 100\text{Mohms}$
- 5) Report noise at VBGP terminal (expected 25uV rms)