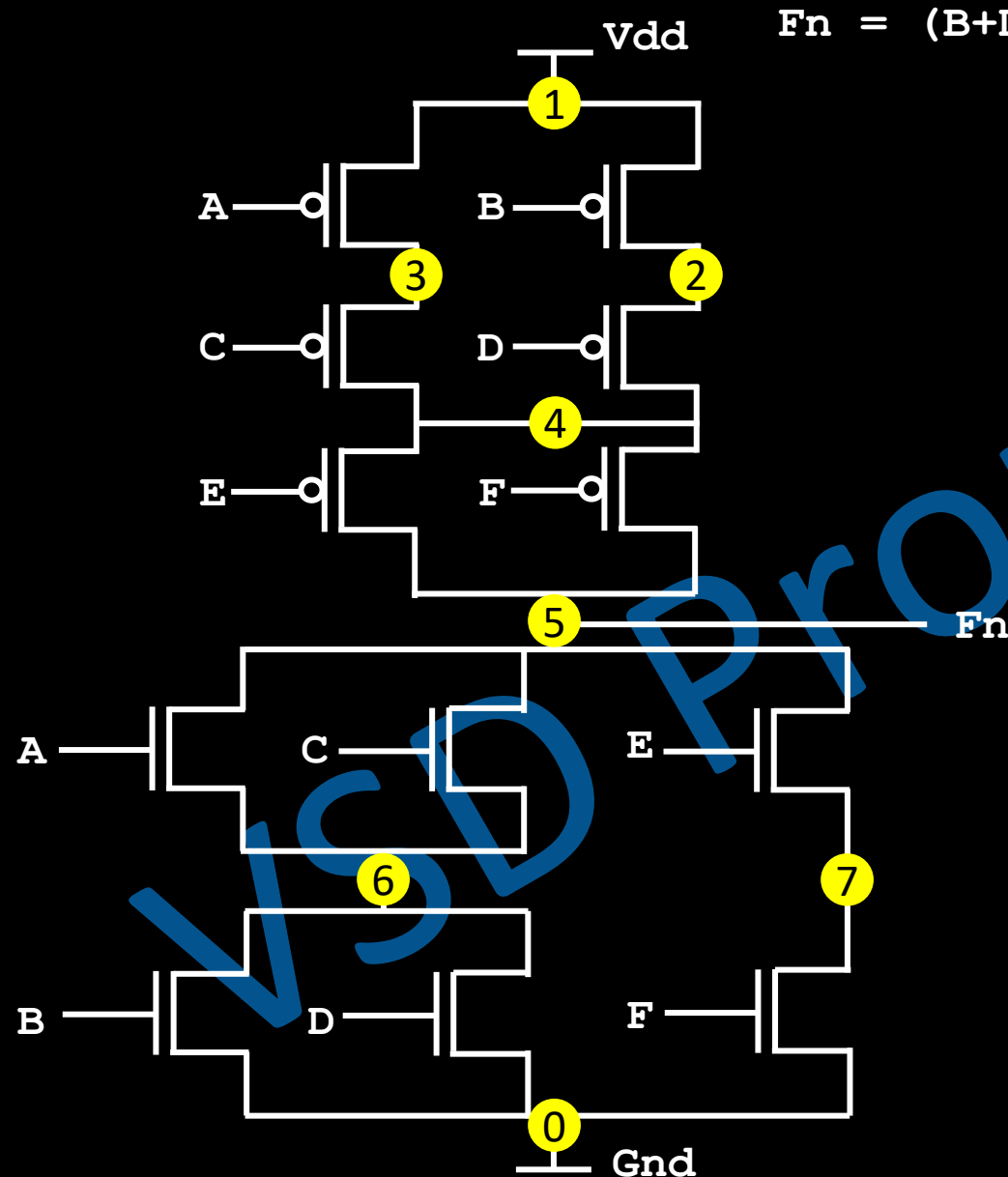


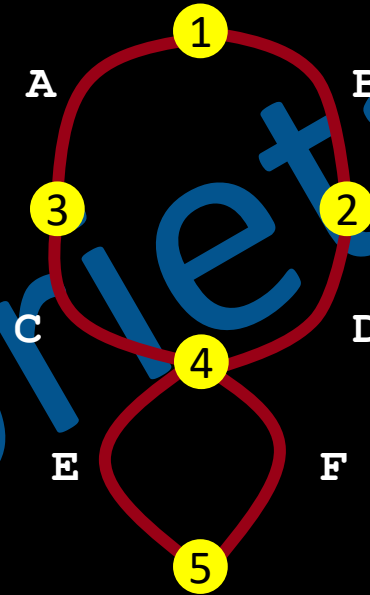
DISCLAIMER by VSD Corp. Pvt. Ltd.

- **Original Specs by VSD Corp. Pvt. Ltd.**
- **To be used only for Educational purposes**
- **Please contact Kunal at kunalpghosh@gmail.com in case of any doubts**
- **You must develop a tool or engine using TCL/PERL/Python or any scripting/programming language that does what is mentioned in following slides**

Art of layout - Euler's path and stick diagram

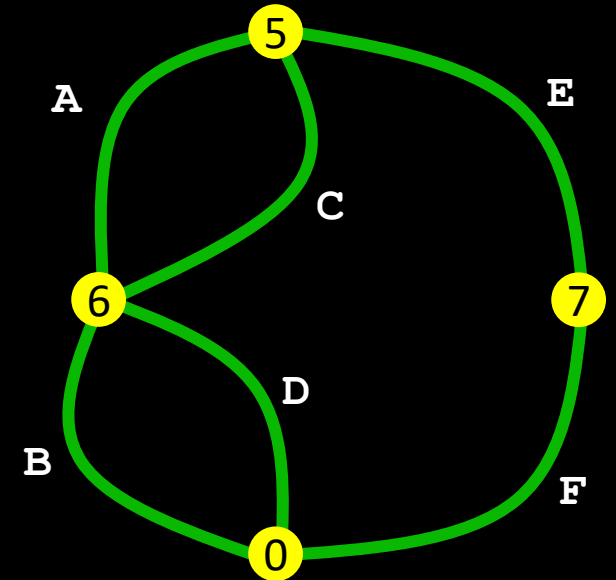


$$F_n = (B + D) \cdot (A + C) + E \cdot F$$



pmos network graph

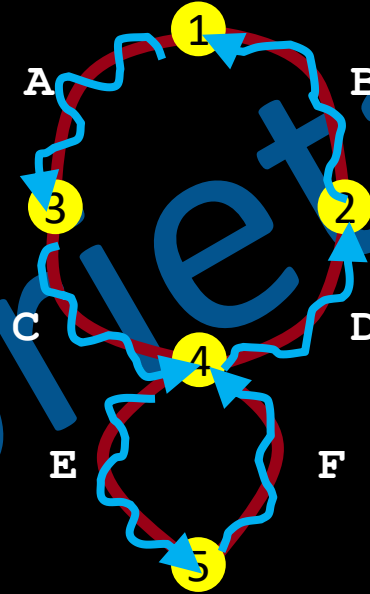
nmos network graph



Art of layout - Euler's path and stick diagram

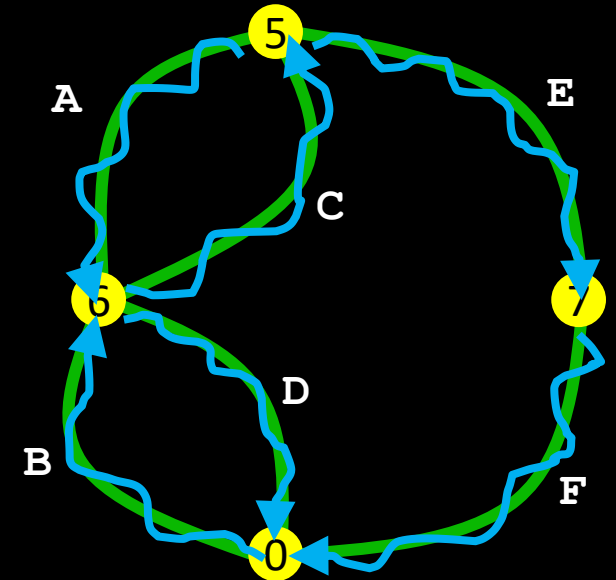
$$F_n = (B+D) \cdot (A+C) + E \cdot F$$

A - C - E - F - D - B



pmos network graph

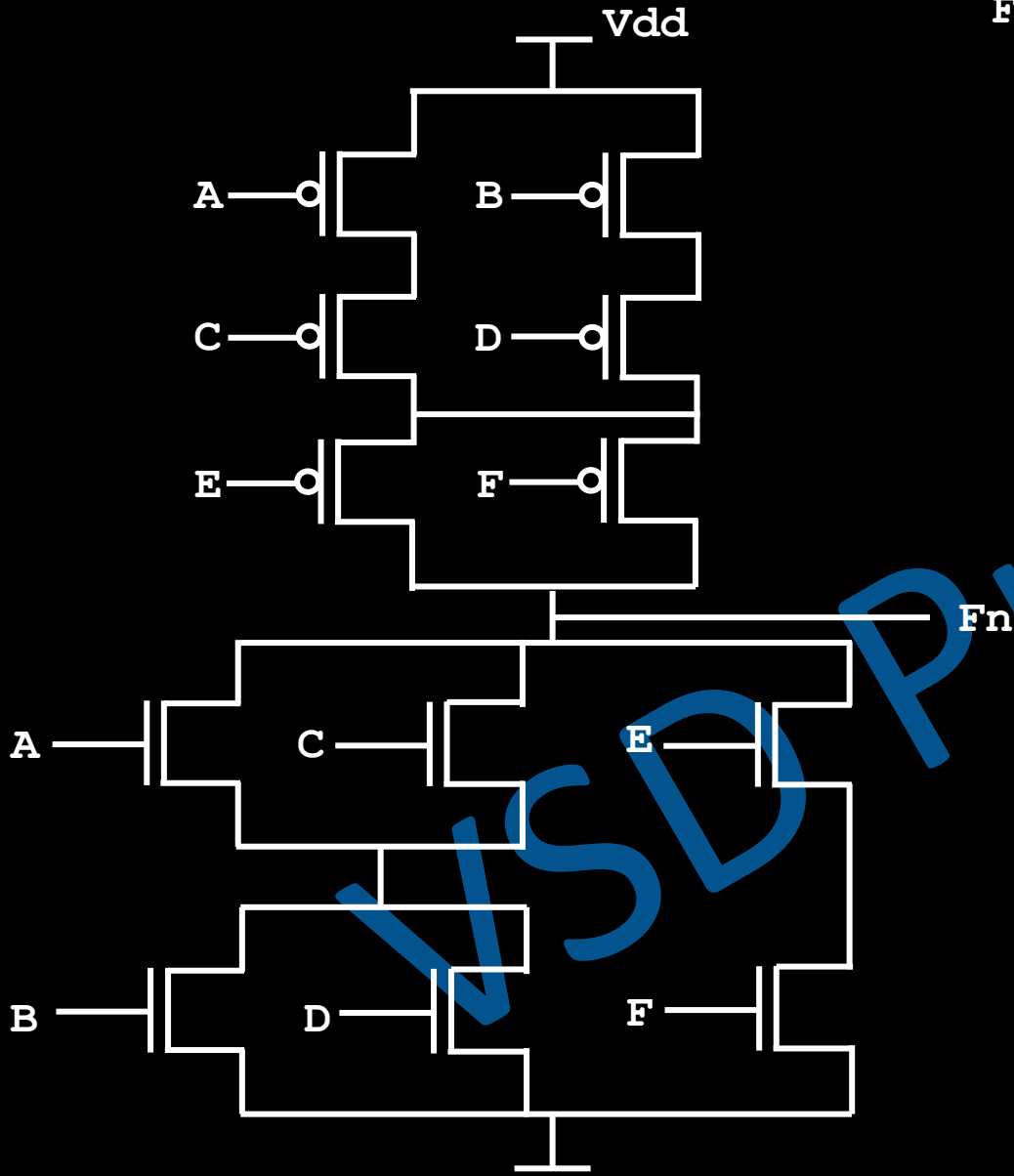
nmos network graph



Art of layout - Euler's path and stick diagram

$$F_n = (B+D) \cdot (A+C) + E \cdot F$$

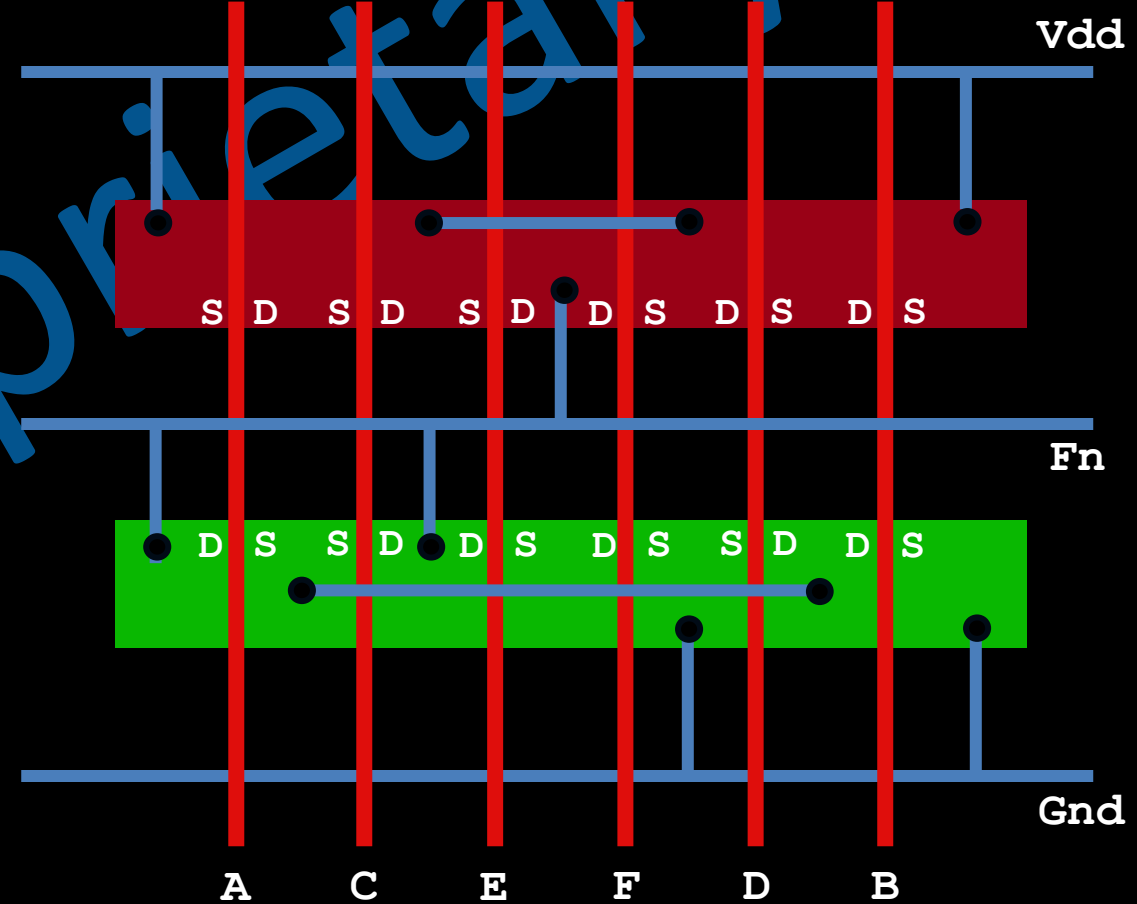
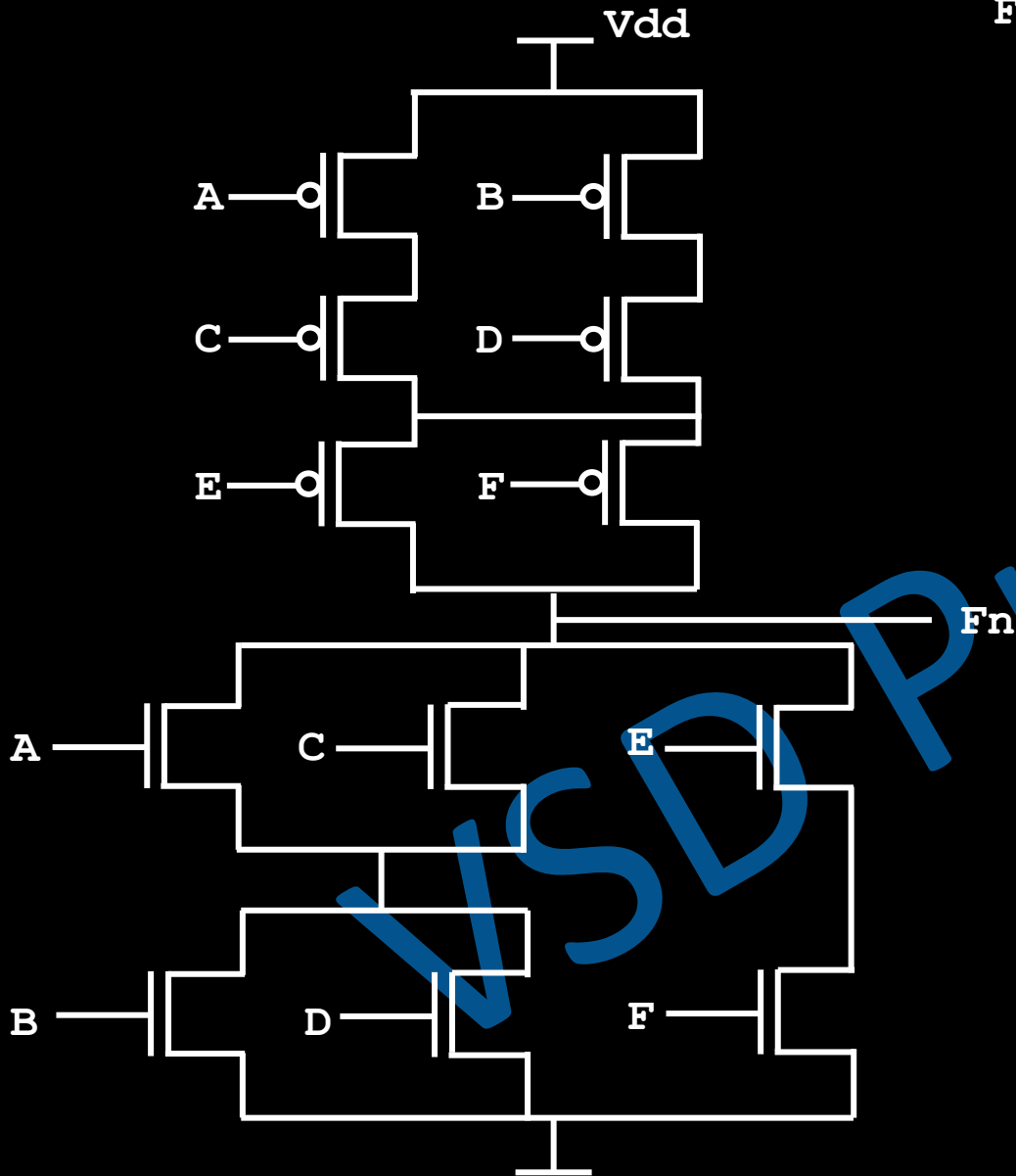
A - C - E - F - D - B



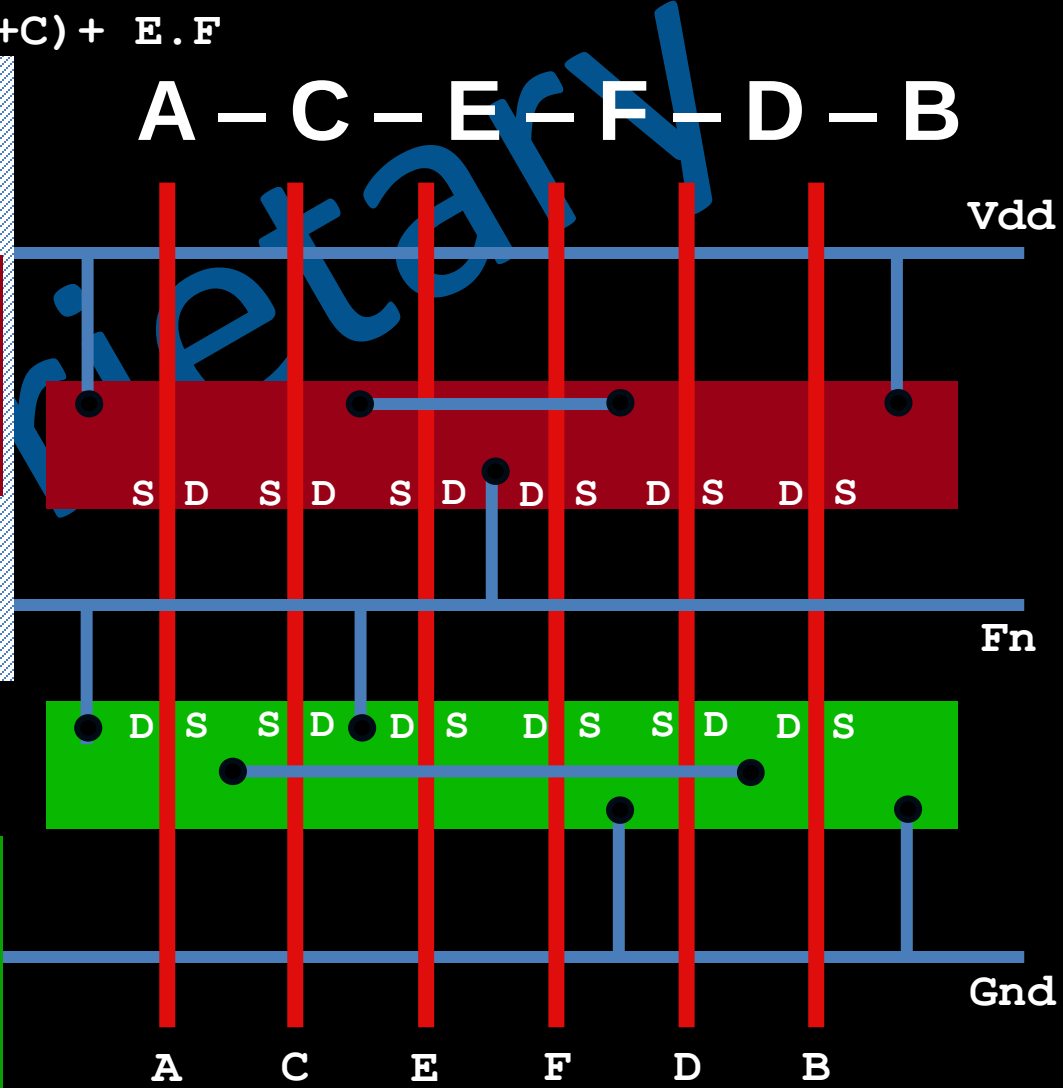
Art of layout - Euler's path and stick diagram

$$F_n = (B+D) \cdot (A+C) + E \cdot F$$

A - C - E - F - D - B

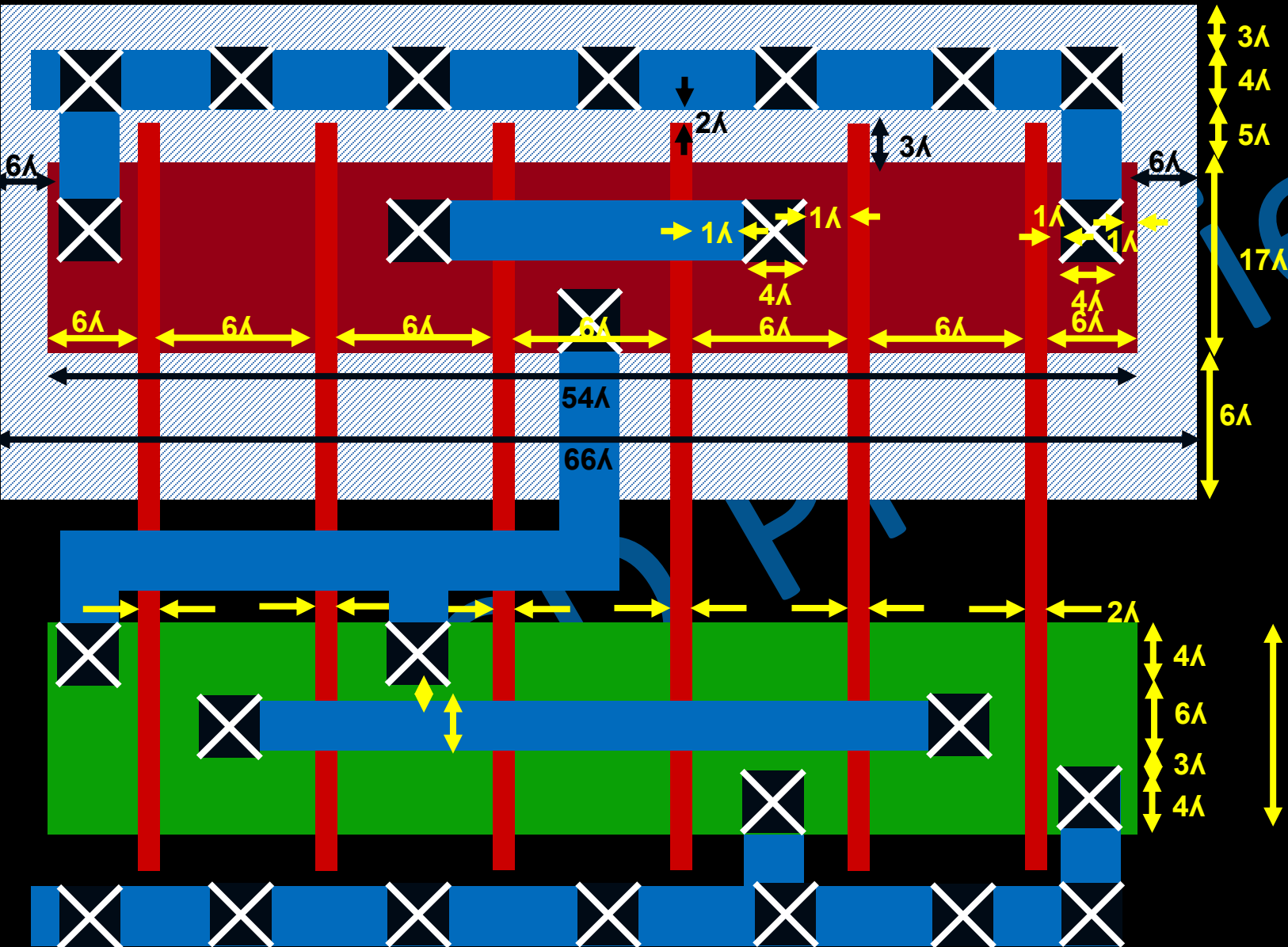


11 12 13 14 15 16 17 18 19 20 21 22 23 24 25



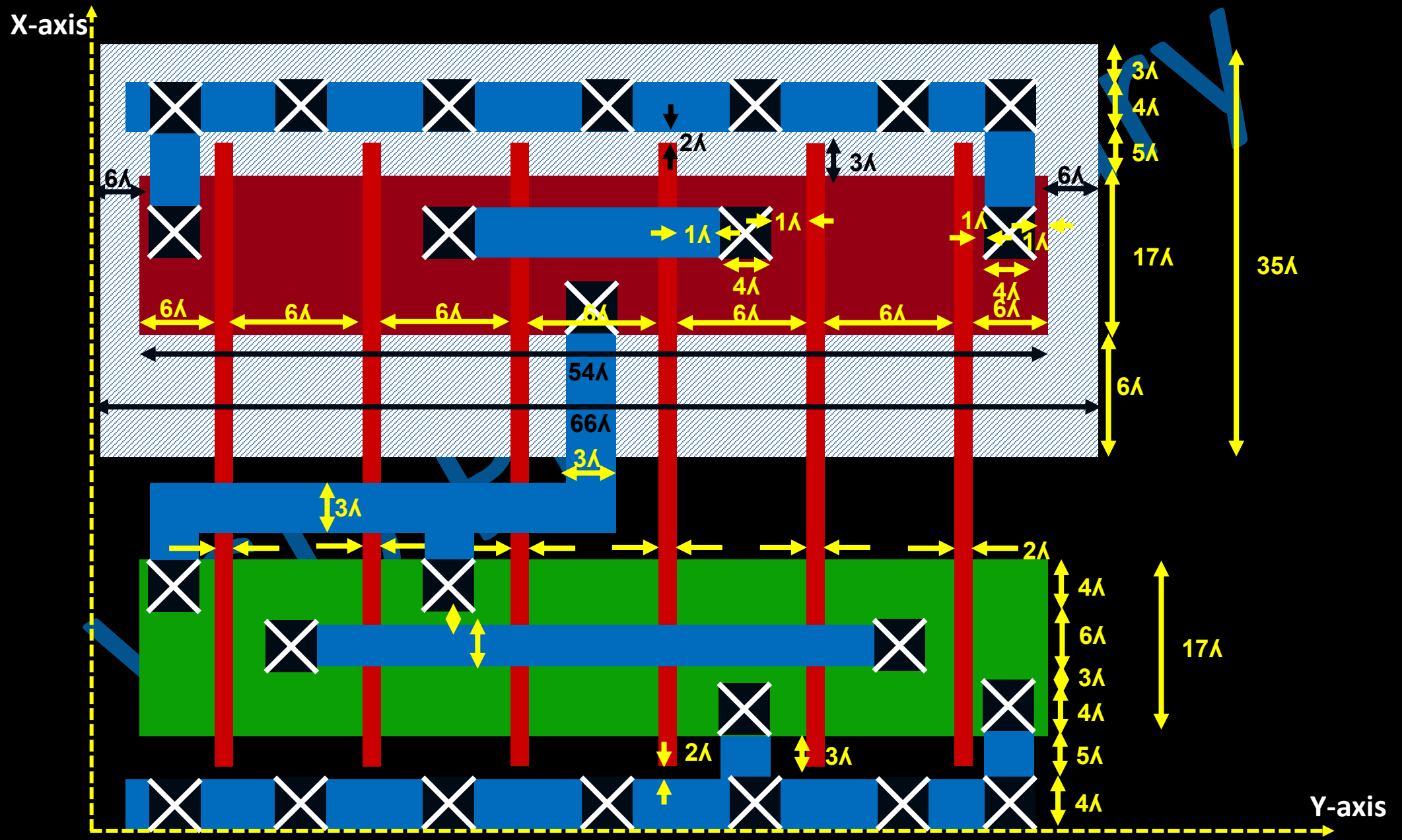
Art of layout – Euler's path and stick diagram

A – C – E – F – D – B



details (min)	Design rule
poly width	2λ
extension over active	3λ
poly to active spacing	1λ
poly to ndc spacing	1λ
ndc width	4λ
metal width	3λ
poly to metal spacing	n/a
ndc to ndc spacing	2λ
active/diffusion width	3λ

Art of layout – Euler's path and stick diagram

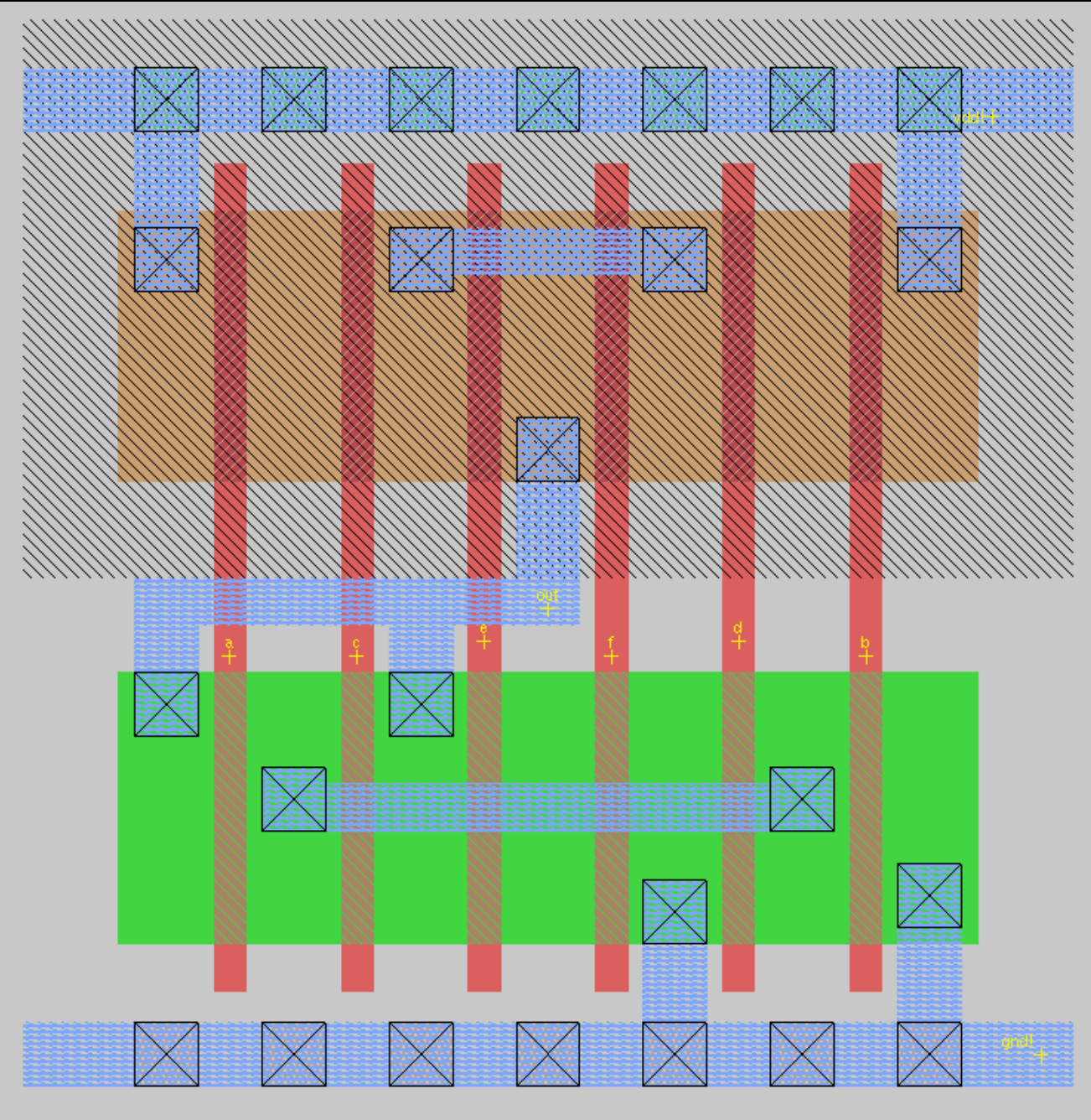
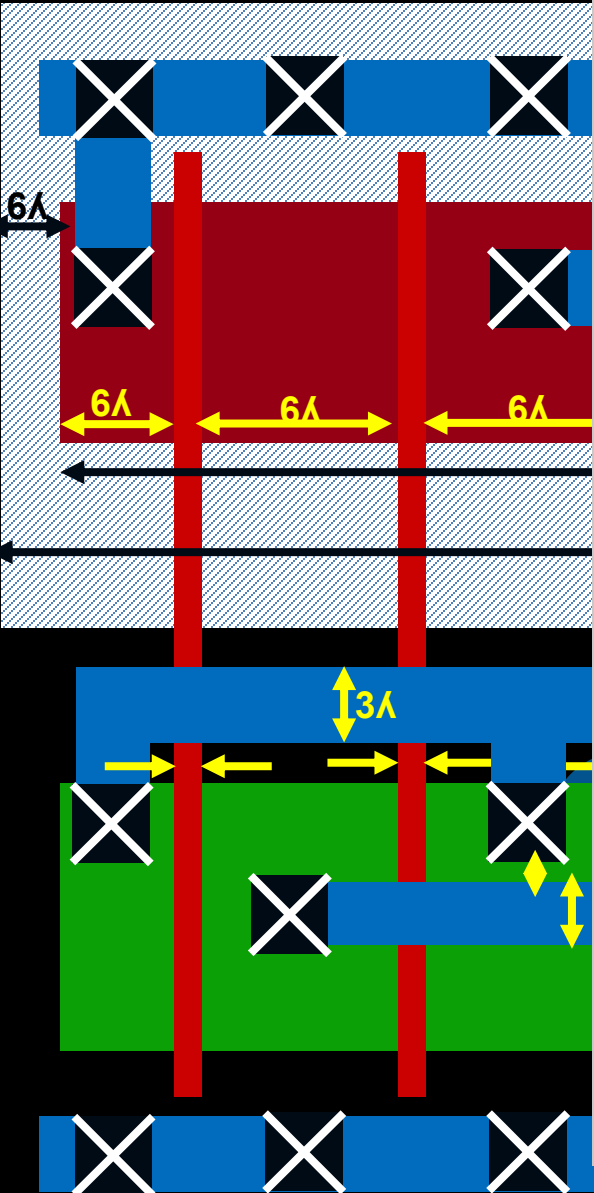


Art of 1

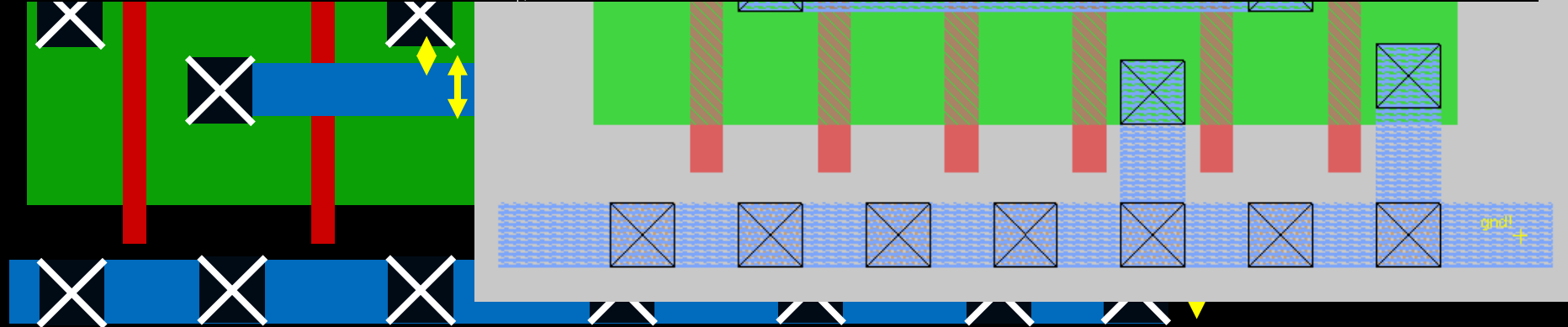
Diagram

E - F - D - B

ary



Pre-layout spice simulation

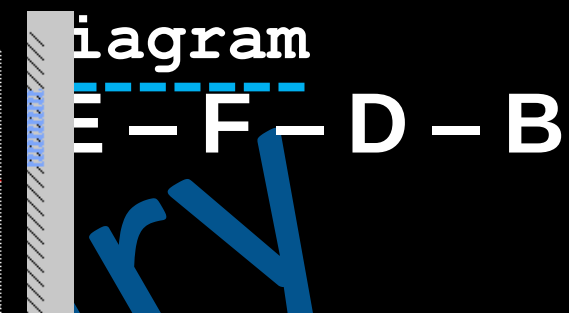
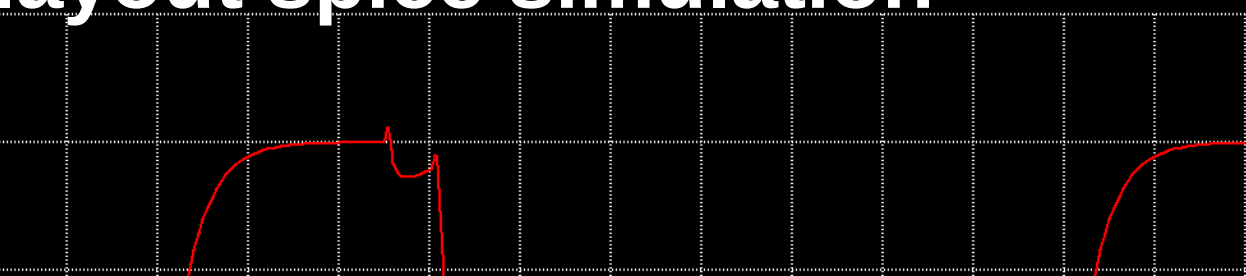
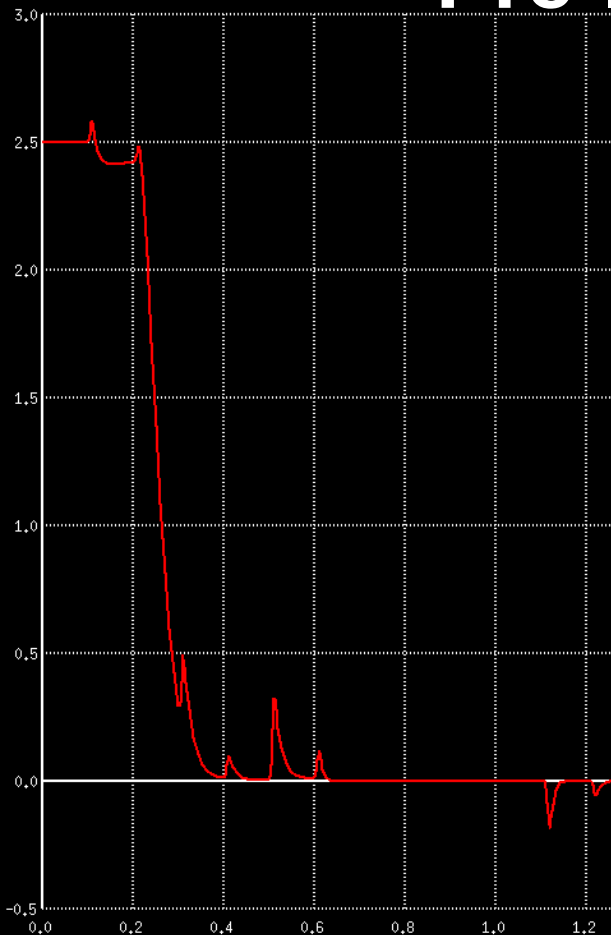


Diagram

E - F - D - B

ary

Pre-layout spice simulation



Post-layout spice simulation

